



UB

Q. P. Code: 24393

(3 Hours)

[Total Marks: 80]

- N.B.: 1) Question No. 1 is Compulsory.  
 2) Answer any **THREE** questions from Q.2 to Q.6.  
 3) Figures to the right indicate full marks.

Q.1. a) Evaluate the Laplace transform of  $\sqrt{1 + \sin t}$  [5]

b) Find directional derivative of  $\phi = 4x^2 + x^2yz$ , at  $(1, -2, -1)$  in direction of  $2i - j - 2k$  [5]

c) Find orthogonal trajectories of the family of curves  $e^x \cos y - xy = c$ . [5]

d) Obtain half range sine series for  $f(x) = x$ ,  $0 < x < 2$ . [5]

Q.2. a) If  $u + v = e^{2x}(x \cos 2y - y \sin 2y)$  then find analytic function  $f(z)$  by Milne Thomson Method [6]

b) Find the Fourier series for  $f(x) = 9 - x^2$ ,  $-3 \leq x \leq 3$  [6]

c) Find the Laplace transform of the following

i)  $L\{x\sqrt{1 + \sin x}\}$       ii)  $L\left\{\frac{\sinh 2x}{x}\right\}$  [8]

Q.3. a) Using Convolution theorem, find Inverse Laplace of  $\frac{x}{(x^2 + 4)^2}$ . [6]

b) Prove that  $\int_{-\pi/2}^{\pi/2} f(x) dx = \int_{-\pi/2}^{\pi/2} \frac{2-x}{x} \sin x + \frac{(3-x^2)}{x^2} \cos x dx$ . [6]

c) Find Fourier series for  $f(x) = (\pi - x)^2$  in  $0 < x < 2\pi$ . Hence deduce that

$$\frac{\pi^2}{8} = 1 - \frac{1}{3^2} + \frac{1}{5^2} - \dots$$
 [8]

Q.4 a) Find the Fourier transform of  $f(t) = e^{-t}$  [6]

b) Show that the function  $f_1(x) = 1$ ,  $f_2(x) = x$  are orthogonal on  $(-1, 1)$  and determine the

constant A & B so that functions  $f_3(x) = 1 + Ax + Bx^2$  is orthogonal to both  $f_1(x)$  and

$f_2(x)$  on that interval. [6]

c) Find bilinear transformation which maps the points  $z=1, i, -1$  onto the points  $w=i, 0, -i$  hence



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find the image of  $|z| < 1$  on to  $w$  plane find invariant points of this transformation. [8]

Q 5 a) Solve using Laplace Transform  $\frac{d^2 y}{dt^2} + 2\frac{dy}{dt} + y = te^{-t}$  given  $y(0) = 4$  and  $y'(0) = 2$ . [6]

b) Find Complex form of the Fourier series for  $f(x) = e^{ax}$  in  $-\pi < x < \pi$  where 'a' is a

real constant. Hence deduce that  $\frac{\pi}{a \sinh a\pi} = \sum_{n=-\infty}^{\infty} \frac{(-1)^n}{n^2 + a^2}$  [6]

c) Verify Green's Theorem in the plane for  $\int_C (3x^2 - 8y^2) dx + (4y - 6xy) dy$  where  $C$  is

the boundary of the region defined by  $y = x^2$  and  $y = \sqrt{x}$ . [8]

Q 6. a) Prove that  $J_n'(x) = J_{n-2}(x) - 2J_n(x) + J_{n+2}(x)$  [6]

b) Find the map of the line  $x-y=1$  by transformation  $w = \frac{1}{z}$  [6]

c) Evaluate  $\iiint_S \vec{F} \cdot d\vec{s}$  where  $\vec{F} = 4x^2 \hat{i} - 2y^2 \hat{j} + z^2 \hat{k}$  where  $S$  is the region bounded by

$x^2 + y^2 = 4$ ,  $z = 0$ ,  $z = 3$  using Gauss divergence theorem. [8]

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Max. Marks:80

Duration: 3hrs

NB:

- (1) Question No.1 is compulsory.
- (2) Answer any three from remaining questions.
- (3) Figures to the right indicate full marks.
- (4) Assume suitable data if required.



Q.1 Attempt any four

- a Give the equation for the current in semiconductor diode. With the help of this equation explain in detail the V-I characteristics of a semiconductor diode. 5
- b Explain effect of temperature on JFET and derive equation for zero temperature drift. 5
- c For the circuit shown in fig 1 determine small signal hybrid pi parameters of transistor. 5

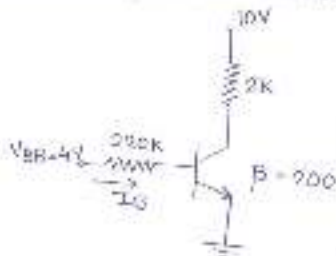


Fig.1

- d Design clipper circuit for the output shown in figure.2 Assume diode is ideal.

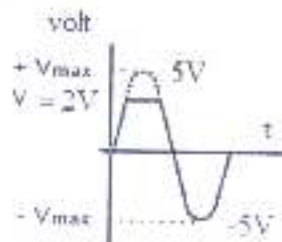


Fig.2



Fig.3

- e For the FET shown in figure.3 the drain current equation is  $I_{DQ} = 9 \left( 1 + \frac{V_{GSQ}}{V_p} \right)^2$  mA. Determine  $I_{DQ}$ ,  $V_{GSQ}$ ,  $V_{DSQ}$ ,  $V_{U}$ ,  $V_{DD}=18V$ ,  $R_D=2.2k\Omega$ ,  $R_S=1.2K\Omega$ ,  $-V_{SS}=-9V$  and  $R_G=1M\Omega$ . 5

Q.P. Code : 13607

( 3 Hours)

[ Total marks : 80

- Note :-
- 1) Question number 1 is **compulsory**.
  - 2) Attempt any **three** questions from the remaining **five** questions.
  - 3) **Figures** to the right indicate **full marks**.

- Q.1
- a) Find the Laplace transform of  $\cos t \cos 2t \cos 3t$ . 05
  - b) Construct an analytic function whose real part is  $e^x \cos y$ . 05
  - c) Find the directional derivative of  $\phi = x^4 + y^4 + z^4$  at point  $A(1, -2, 1)$  in the direction of  $AB$  where  $B$  is  $(2, 6, -1)$ . 05
  - d) Expand  $f(x) = lx - x^2$ ,  $0 < x < l$  in a half-range sine-series. 05
- Q.2
- a) Find the angle between the normals to the surface  $xy = z^2$  at the points  $(1, 4, 2)$ ,  $(-3, -3, 3)$ . 06
  - b) Find the Fourier series for 06  

$$f(x) = \begin{cases} -c & -a < x < 0 \\ c & 0 < x < a \end{cases}$$
  - c) Find the inverse Laplace transform of 08  
 (i)  $\frac{4s + 12}{s^2 + 8s + 12}$   
 (ii)  $\log\left(\frac{s^2 + a^2}{\sqrt{s + b}}\right)$
- Q.3
- a) State true or false with proper justification "There does not exist an analytic function whose real part is  $x^3 - 3x^2y - y^3$ ". 06
  - b) Prove that  $J_{5/2}(x) = \sqrt{\frac{2}{\pi x}} \left( \frac{3-x^2}{x^2} \sin x - \frac{3}{x} \cos x \right)$ . 06
  - c) Expand  $f(x) = 4 - x^2$  in the interval  $(0, 2)$ . 08
- Q.4
- a) Use Gauss's Divergence theorem to evaluate  $\iint_S \vec{N} \cdot \vec{F} dS$  where  $\vec{F} = 4x \vec{i} + 3y \vec{j} - 2z \vec{k}$  and  $S$  is the surface bounded by  $x = 0, y = 0, z = 0$  and  $2x + 2y + z = 4$ . 06



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b) Prove that 06  

$$\int x^3 \cdot J_0(x) dx = x^3 \cdot J_1(x) - 2x^2 \cdot J_2(x).$$

c) Solve using Laplace transform  $\frac{dy}{dt} + 3y = 2 + e^{-t}$  with 08  
 $y(0) = 1.$

Q. 5 a) Find Laplace transform of  $(1 + 2t - 3t^2 + 4t^3)H(t - 2)$  where 06

$$H(t - 2) = \begin{cases} 0, & t < 2 \\ 1, & t \geq 2 \end{cases}$$

b) Prove that  $2J_0''(x) = J_2(x) - J_0(x).$  06

c) Obtain complex form of Fourier Series for  $f(x) = e^{ax}$  in  $(-\pi, \pi)$  08  
 where  $a$  is not an integer. Hence deduce that when  $a$  is a constant other than an integer

$$\sin ax = \frac{\sin \pi a}{i\pi} \sum \frac{(-1)^n n}{(a^2 - n^2)} e^{inx}$$

Q. 6 a) Using Green's theorem evaluate 06

$$\oint_C (e^{x^2} - xy) dx - (y^2 - ax) dy$$

where  $C$  is the circle  $x^2 + y^2 = a^2.$

b) Express the function 06

$$f(x) = \begin{cases} 1 & \text{for } |x| < 1 \\ 0 & \text{for } |x| > 1 \end{cases}$$

as a Fourier Integral.

c) Under the transformation  $w = (1 + i)z + (2 - i)$ , find the region 08  
 in the  $w$ -plane into which the rectangular region bounded by  $x = 0, y = 0, x = 1, y = 2$  in the  $z$ -plane is mapped.



XXX XXX

Time: 3 Hours

Marks: 80

- N.B. : (1) Question No. 1 is compulsory.  
 (2) Solve any three questions from the remaining five  
 (3) Figures to the right indicate full marks  
 (4) Assume suitable data if necessary and mention the same in answer sheet.

Q.1 Attempt any 5 questions

[20]

- a) Prove that for a JFET the gate-source bias for zero temperature drift of drain current is at  $|V_{gs}| = 0.63$  volts.
- b) Explain the hybrid pi model of BJT.
- c) Explain Zener as voltage regulator.
- d) Consider a BJT has parameters  $f_T = 500\text{MHz}$  at  $I_C = 1\text{mA}$ ,  $\beta = 100$  and  $C_{\mu} = 0.3\text{pF}$ . Calculate bandwidth of  $f_{\beta}$  and capacitance  $C_{\pi}$  of a BJT.
- e) Draw and explain small-signal model of a diode.
- f) Why should  $R_C$  be as large as possible in the design of CE amplifier?
- Q.2 a) Design a voltage divider bias network using a supply of 24 V, a transistor with  $\beta = 110$  and an operating point of  $I_{CQ} = 4\text{mA}$  and  $V_{CEQ} = 8\text{V}$ . Assume  $V_{gs} = \frac{1}{8}V_{gs}$ . [10]
- b) Explain the fabrication steps of passive elements. [5]
- c) What are the important JFET parameters and define it from characteristics. [5]
- Q.3 a) Design the resistors of a single stage CS amplifier for audio frequency with BFW11 with  $I_{CQ} = (3.3 \pm 0.6)\text{mA}$  and  $|A_v| = 12$ . [10]
- b) Draw CS JFET amplifier with self bias circuit and derive the expression for voltage gain input impedance and output impedance. [10]
- Q.4 a) Draw small signal hybrid parameter equivalent circuit for CE amplifier and define the same. What are the advantages of h parameters? [10]
- b) For the circuit shown below in Fig.4b, the transistor parameters are  $V_{BE} = 0.7\text{V}$ ,  $\beta = 200$  and  $V_A = \infty$ . [10]
- i) Derive the expression for lower cut-off frequency (or time constant) due to input coupling capacitor.
- ii) Determine lower cut-off frequency and midband voltage gain.



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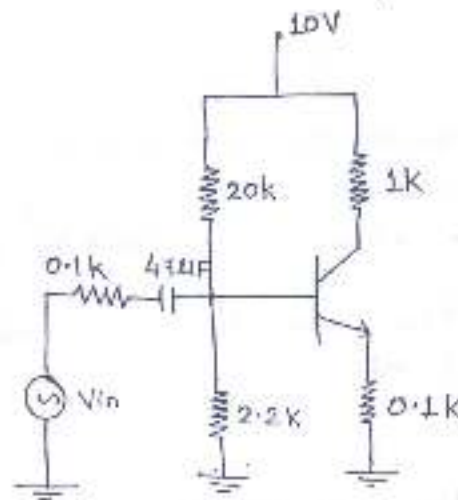


Fig. 4b.

- Q.5 a) Design an L section LC filter with full wave rectifier to meet the following specifications: The DC output voltage  $V_{dc} = 220$  V, deliver  $I_L = (70 \pm 20)$  mA to the resistive load and the required ripple factor is 0.04. [10]

- b) For the circuit shown below in Fig. 5b, the transistor parameters are  $V_{BE(on)} = 0.7$  V,  $\beta = 100$  and  $V_A = \infty$ . Determine  $Z_i$ ,  $Z_o$  and  $A_v$ . [10]

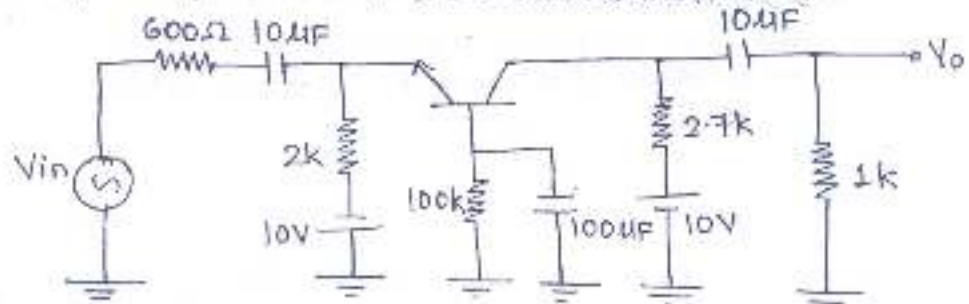


Fig. 5b

- Q.6 Short notes on: (Attempt any four) [20]
- BJT high frequency equivalent circuit
  - Types of resistors and capacitors
  - Stability factors of various biasing techniques of BJT
  - Different types of filters
  - Comparison of BJT CE and JFET CS amplifier

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Transistor type	P <sub>max</sub> (Watt)	I <sub>max</sub> (mA)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	D.C. current gain		Signal gain	λ <sub>h</sub>	V <sub>CE</sub> (V)	Dissip. above 25°C (mW)		
										h <sub>FE</sub>	h <sub>FE</sub>						
3N 3055	115-4	11-0	1-1	100	60	70	90	1	200	20	50	70	15	30	1-6	1-5	0-7
ECN 105	55-0	5-0	1-0	60	50	55	60	5	200	25	50	100	25	75	1-5	3-5	0-4
ECN 149	30-0	4-0	1-0	50	40	—	—	2	250	30	90	110	31	60	1-5	4-0	0-3
ECN 100	5-8	0-7	0-6	70	50	65	—	6	200	30	90	200	30	90	0-9	3-5	0-05
BC147A	0-25	0-1	0-25	40	45	30	—	6	125	115	180	220	125	250	0-9	—	—
2N 325 (PNP)	0-25	0-1	0-25	45	30	—	—	6	100	35	—	50	—	45	—	—	—
BC147C	0-25	0-1	0-25	50	45	30	—	6	125	100	200	450	240	330	100	0-5	—

BFV 11-1000 MUTUAL CHARACTERISTICS

-V <sub>CE</sub> (V)	I <sub>B</sub> (mA)	I <sub>C</sub> (mA)	h <sub>FE</sub>	h <sub>FE</sub>	h <sub>FE</sub>	h <sub>FE</sub>	h <sub>FE</sub>	h <sub>FE</sub>	h <sub>FE</sub>
0-0	0-2	0-6	0-8	1-0	1-2	1-6	2-4	3-5	4-0
1-8	3-8	8-2	1-6	6-8	8-1	5-4	4-2	2-1	2-3
7-8	6-4	1-4	4-6	4-0	3-3	2-7	1-7	0-1	0-2
6-0	3-0	2-3	1-4	1-0	0-5	0-0	0-0	0-0	0-0

BFV 11 (typical)

Type	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	V <sub>CE</sub> (V)	
1N3022	30	50	50	50	100	100	100	100	100	100	100	100	100	100	100	100	100	100
BFV 11 (typical)	30	30	30	30	300	300	300	300	300	300	300	300	300	300	300	300	300	300





(3 Hours)

Total Marks: 80

- N.B. (1) Question No. 1 is **Compulsory**  
 (2) Out of remaining questions, attempt **any three**  
 (3) **Assume** suitable data if required  
 (4) Figures to the right indicate **full marks**
- Q.1 (a) State and prove De Morgan's Theorems [5]  
 (b) Compare Combinational and sequential logic Circuits [5]  
 (c) Define Propagation delay, Power Dissipation, Fan Out, Fan in for TTL family [5]  
 (d) Explain Programmable Logic Array [5]
- Q.2 (a) Prove that NAND and NOR Gates are universal Gates [10]  
 (b) Design a two-bit digital comparator and implement using Gates [10]
- Q.3 (a) Simplify the logical expressions using Boolean Laws and implement using Gates [10]  
 $Y_1 = (A + C)(A + D)(B + C)(B + D)$ ,  $Y_2 = (AB + C)(AB + D)$   
 (b) Implement the given function using 8:1 Multiplexer [10]  
 $F(A, B, C, D) = \sum m(0, 1, 2, 3, 4, 5, 6, 8, 9, 10, 12, 13, 15)$
- Q.4 (a) Explain the working of universal shift register [10]  
 (b) Write a VHDL program to design a 4:1 Mux [10]
- Q.5 (a) Minimize the following expression using Quine McClusky Technique [10]  
 $F(A, B, C, D) = \sum m(1, 3, 7, 9, 10, 11, 13, 15)$   
 (b) Convert JK FF to D FF and T FF to D FF [10]
- Q.6 (a) Design a 3-bit asynchronous counter using JK FF. Draw neat timing diagram [10]  
 (b) Write a note on CPLDs [10]

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(3 Hours)

80 Marks

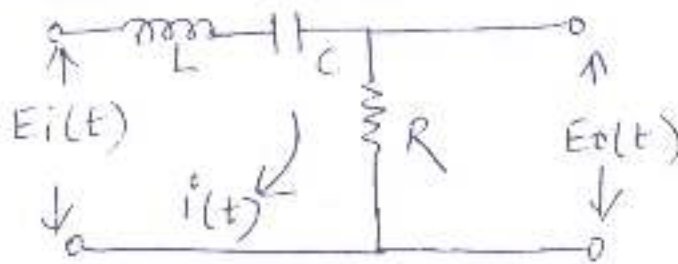
- N.B.: (1) Question No. 1 is compulsory.  
 (2) Solve any **three** questions from the remaining five  
 (3) Figures to the right indicate full marks  
 (4) Assume suitable data if necessary and mention the same in answer sheet.
- Q.1 a) If  $F(A, B, C) = \sum m(0, 3, 5, 7)$  with its truth table and express  $F$  in SOP and POS form [20]  
 b) Compare TTL and CMOS Logic families  
 c) Perform the following operation using 2's complement  
 i)  $(7)_{10} - (15)_{10}$   
 ii)  $(50)_{10} - (2A)_{16}$   
 Comment on results of (i) and (ii)  
 d) Compare SRAM with DRAM
- Q.2 a) Implement following Boolean function using 8:1 multiplexer [10]  
 $F(A, B, C, D) = \bar{A} B \bar{D} + A C D + \bar{B} C D + \bar{A} \bar{C} D$   
 b) Design 3 bit Binary to Gray code Converter [10]
- Q.3 a) What are shift registers? How are they classified? Explain working of any one type of shift register. [10]  
 b) Write VHDL code for 3 bit up counter. [10]
- Q.4 a) Explain Master slave JK Flip flop [5]  
 b) Convert T flip flop to D flip flop. [5]  
 c) Minimize the following expression using Quine McClusky Technique [10]  
 $F(A, B, C, D) = \sum m(1, 3, 7, 9, 10, 11, 13, 15)$
- Q.5 a) State and prove Demorgan's theorem [5]  
 b) Convert  $(532.125)_8$  into decimal, binary and hexadecimal. [5]  
 c) Explain Full Adder circuit using PLA having three inputs, 8 product terms and two outputs. [10]
- Q.6 a) Prove that NAND and NOR gates are universal gates [10]  
 b) Draw and explain 3 bit asynchronous binary counter using positive edge triggered JK flip flop. Draw the waveforms. [10]



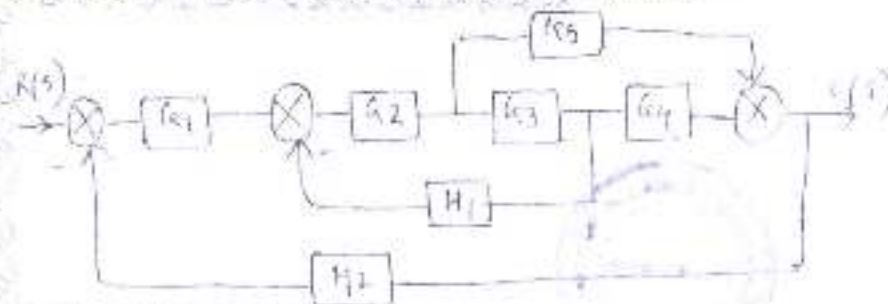
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- N. B. :** (1) Question No. 1 is compulsory.  
 (2) Attempt any **three** questions from **remaining** questions.  
 (3) Assume suitable **data** if **necessary**.

- Q.1 (a) List name of bridges for RLC measurement with proper classification. 04  
 Q.1 (b) Find transfer function of given network. 04



- Q.1 (c) What is cold junction compensation in thermocouples? 04  
 Q.1 (d) Draw a block diagram of generalized data acquisition system and explain its components. 04  
 Q.1 (e) Check whether given system is stable  
 $s^6 + 3s^5 + 2s^4 + 9s^3 + 5s^2 + 12s + 20 = 0$  04  
 Q.2 (a) Explain Kelvin's double bridge and its application in low resistance measurement. 05  
 (b) Obtain  $C[s] / R[s]$  using block diagram reduction technique 10



- Q.3 (a) For unity gain system having  

$$G(s) = \frac{K}{s(s+5)(s+3)}$$
 Sketch root locus and comment on stability. 10  
 (b) Draw Bode plot for following transfer function is  

$$G(s)H(s) = \frac{800}{s^2(s+10)(s+40)}$$
 And predict stability. 10

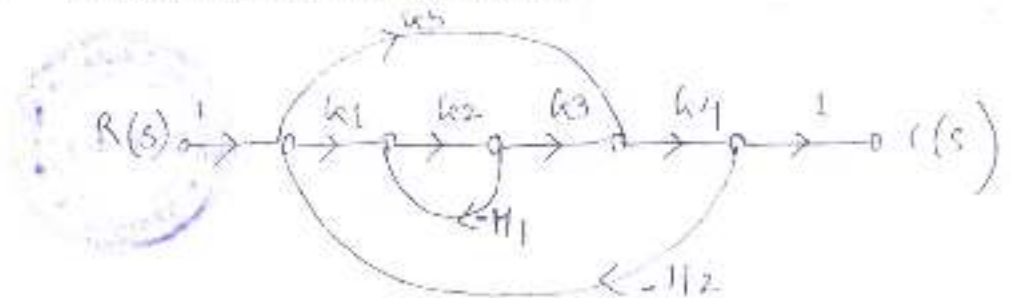
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- Q.4 (a) What is multiplexing? compare FDM with TDM
- (b) The system has

$$G(S)H(S) = \frac{K}{S(S+2)(S+4)(S+8)}$$

Using Routh criterion find range of K for stability.

- (c) Explain working of strain gauge and its application in load measurement.
- Q.5 (a) Find C(s)/R(s) using Mason's gain formula



- (b) Draw and discuss Hay bridge and its application in measurement of inductance.
- Q.6 (a) Explain landline telemetry and discuss about any one landline telemetry system.
- (b) For a system with transfer function  $\frac{64}{s^2+5s+64}$  with unit step input  
Find damping ratio, damped frequency of oscillations and time for peak overshoot.
- (c) Compare temperature transducers Thermistors and thermocouples on the basis of principle, characteristics, ranges and applications.
- (d) Explain how the stability of system is analyzed using Nyquist criteria.

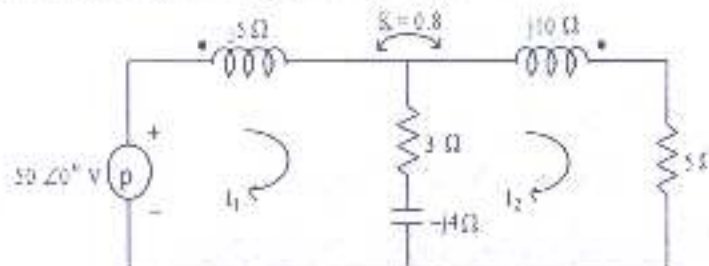


N.B.

- 1) Question No. 1 is Compulsory
- 2) Out of remaining questions, attempt any three
- 3) Assume suitable data if required
- 4) Figures to the right indicate full marks

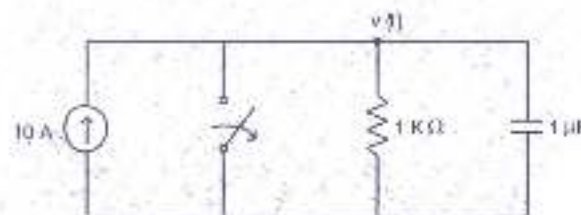


- 1 (A) Draw equivalent circuit for given magnetically coupled circuit.



- (B) In the given network of Fig., switch is opened at  $t = 0$ . Solve for  $v$  and  $\frac{dv}{dt}$  at  $t = 0^+$ .

05



- (C) Prove that  $AD - BC = 1$  for Transmission parameters.

05

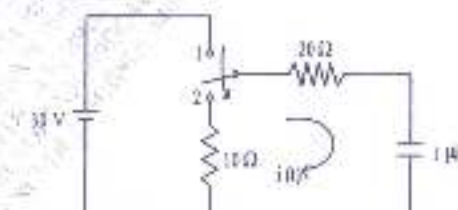
- (D) Define the following parameter of transmission lines:

05

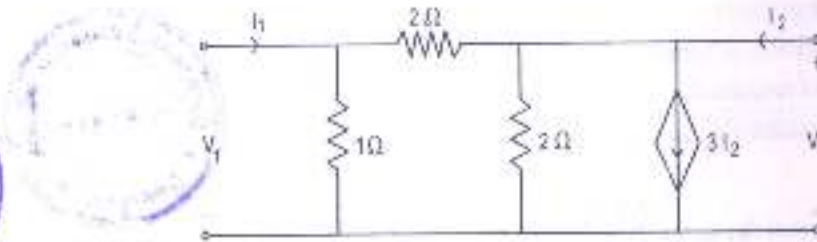
- i) Input impedance
- ii) Characteristics Impedance
- iii) VSWR
- iv) Reflection Coefficient
- v) Transmission Coefficient

- 2 (A) In the network shown in Fig., switch is changed from position 1 to position 2 at  $t = 0$ , steady condition having reached before switching. Find the values of  $i$ ,  $\frac{di}{dt}$  and  $\frac{d^2i}{dt^2}$  at  $t = 0^+$ .

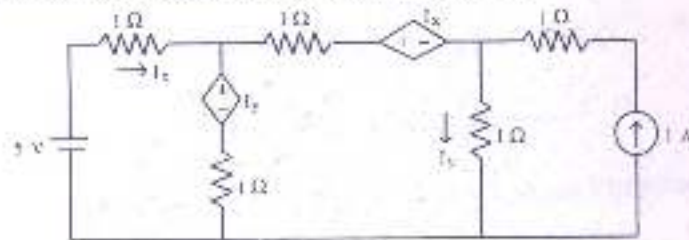
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(B) For the network shown in Fig., find Z and Y-parameters.



3 (A) Find currents in the three meshes of network shown in Fig.



(B) The parameters of a transmission lines are  $R = 65\Omega/\text{km}$ ,  $L = 1.6\text{mH}/\text{km}$ ,  $G = 2.25 \text{ mmho}/\text{km}$ ,  $C = 0.1\mu\text{F}/\text{km}$ . Find

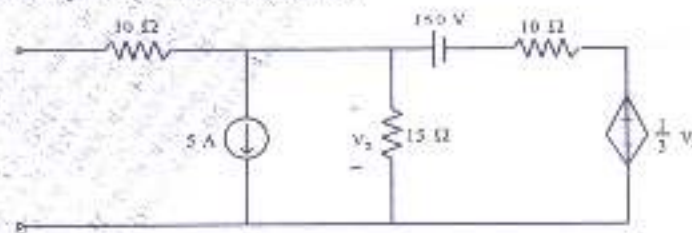
- i) Characteristic Impedance
- ii) Propagation Constant
- iii) Attenuation Constant
- iv) Phase Constant at 1 kHz

4 (A) Determine whether following functions are positive real

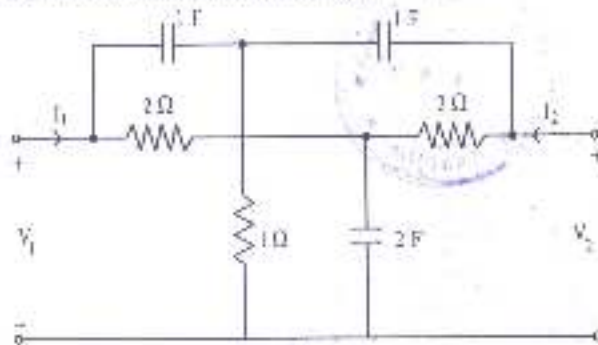
i) 
$$\frac{s^4 + 3s^3 + s^2 + s + 2}{s^3 + s^2 + s + 1}$$

ii) 
$$\frac{s(s+3)(s+5)}{(s+1)(s+4)}$$

(B) Obtain Thevenin equivalent network of Fig.



5 (A) Find Y-parameters for the network shown in Fig. 10

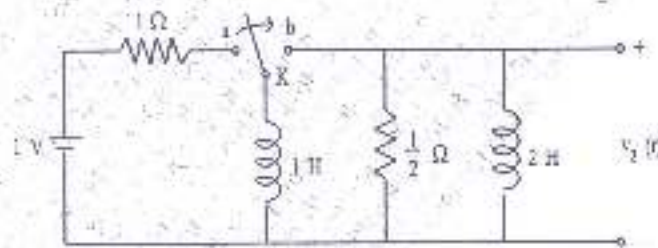


(B) Realize the following functions in Foster II and Cauer I form 10

$$Z(s) = \frac{2(s^2 + 1)(s^2 + 9)}{s(s^2 + 4)}$$

6 (A) A transmission line has a characteristics impedance of 50 ohm and terminate in a load  $Z_L = 25 + j50$  ohm. Use smith chart and Find VSWR and Reflection coefficient at the load. 10

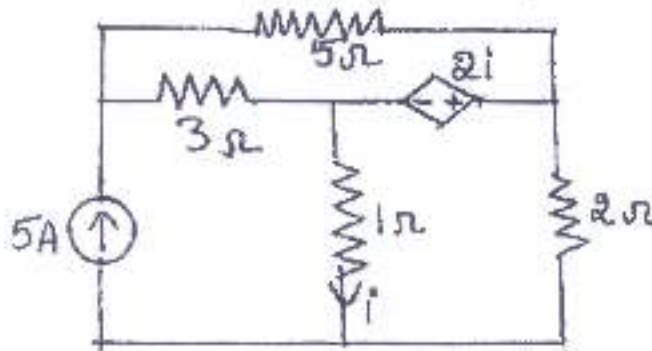
(B) In the network of Fig. switch is in position 'a' for a long time. At  $t = 0$  switch is moved from 'a' to 'b'. Find  $v_2(t)$ . Assume that the initial current in 2 H inductor is zero. 10



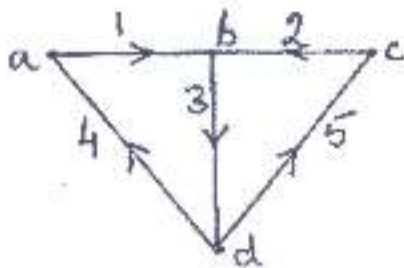
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- N.B:** 1. Question No. 1 is compulsory.  
2. Attempt any three from the remaining questions.

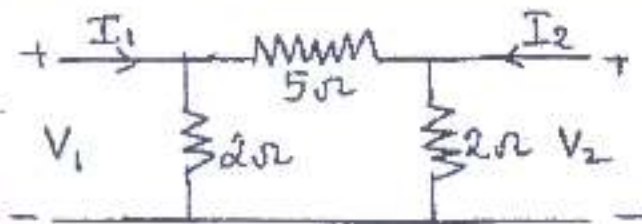
1. (a) Find the voltage drop across  $5\Omega$  resistor in the circuit given below. 5



- (b) For the graph given below obtain the incidence matrix and find the number of possible trees. 5



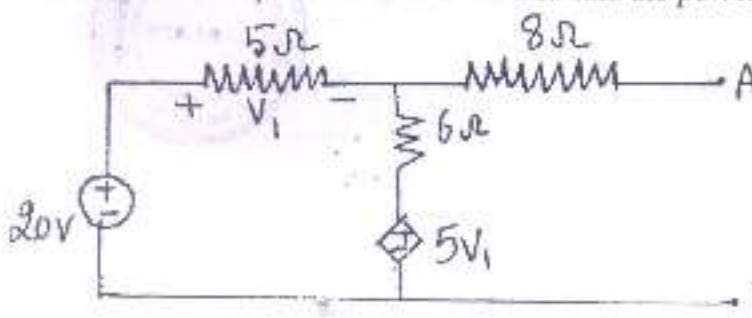
- (c) Find y parameters for the two-port network shown in figure. 5



- (d) Check whether the following polynomials are Hurwitz 5

- (i)  $P(s) = s^4 + 7s^3 + 6s^2 - 21s + 8$   
(ii)  $P(s) = s^5 + 2s^3 + s$

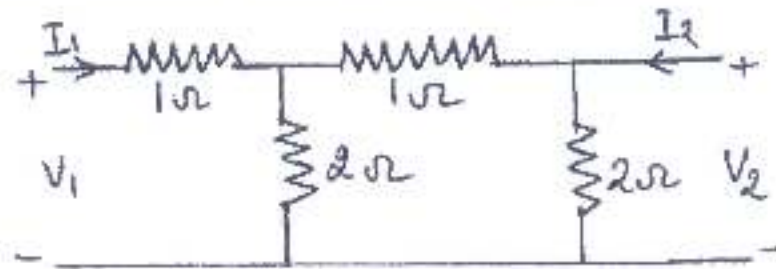
- (a) Find the Thevenin's equivalent across AB and find the power dissipated in a  $25\Omega$  load. 10



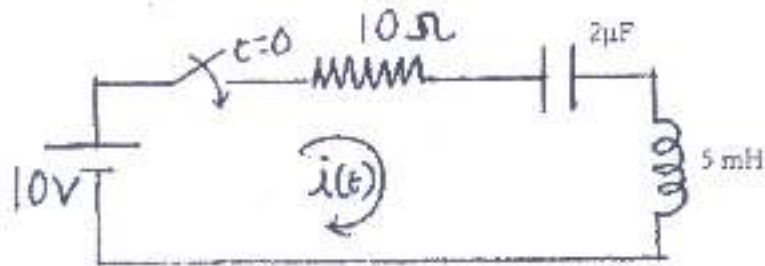
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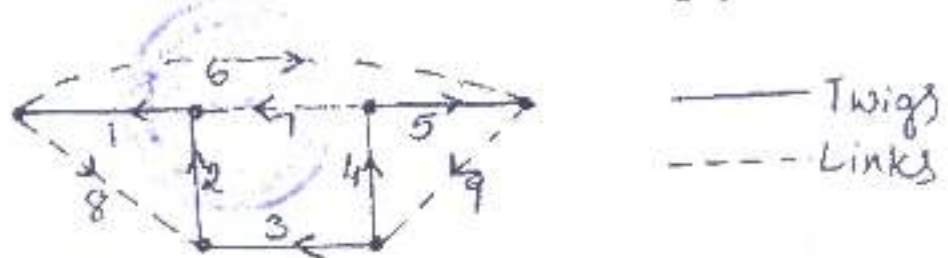
- (b) Find h parameters for the following Two-port network.



- (c) In the network shown below the switch is closed at  $t = 0$ . Assuming all initial conditions to be zero, find  $i$ ,  $di/dt$ ,  $d^2i/dt^2$  for  $t = 0^+$ .



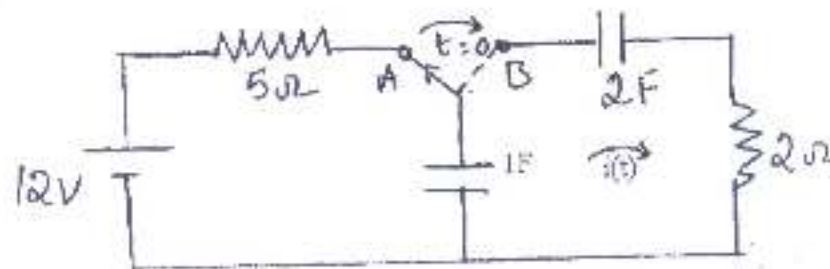
3. (a) Find the tie-set and f-cutset matrix for the oriented graph shown below. 10



- (b) Realize the following function in Foster I and Foster - II forms. 10

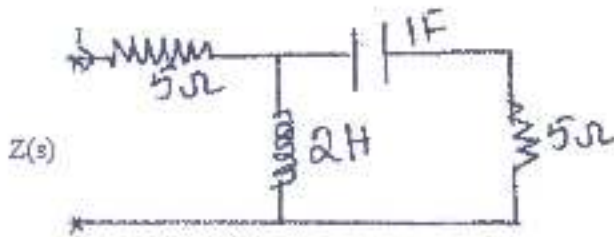
$$Z(s) = \frac{(s+1)(s+4)}{s(s+2)}$$

4. (a) A switch is in position A for a long time and then thrown to position B at  $t = 0$ . Find  $i(t)$  for  $t > 0$ . At what value of 't' the current  $i(t)$  will become half of current at  $t = 0$  10

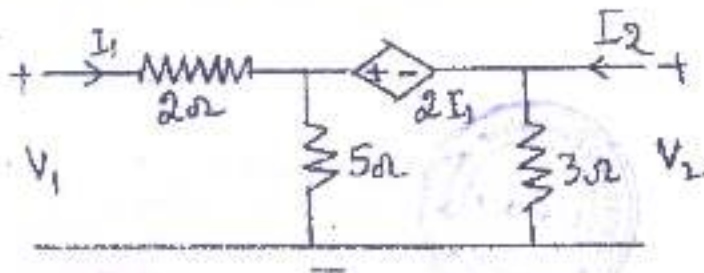


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- (b) For the following network find the driving point impedance function. 5



- (c) Find the condition for symmetry and reciprocity for a two port network using any one parameter. 5
5. (a) Obtain the ABCD parameters of the following network. If two such networks are cascaded find the overall ABCD parameter. 10



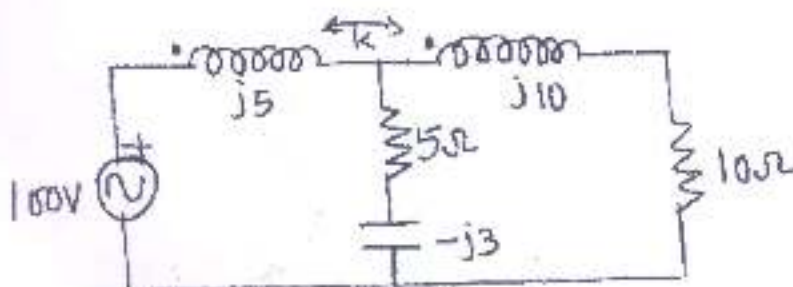
- (b) Check whether the following function is positive real or not. 5

$$F(s) = \frac{(s^2 + 6s + 5)}{(s^2 + 9s + 14)}$$

- (c) Find the oriented graph if the incidence matrix of the network is as given below. 5

$$A = \begin{bmatrix} 1 & 0 & 0 & 1 & 1 & 0 & 0 \\ 0 & -1 & 1 & 0 & -1 & 0 & 0 \\ -1 & 1 & 0 & 0 & 0 & 0 & -1 \\ 0 & 0 & 0 & -1 & 0 & 1 & 0 \end{bmatrix}$$

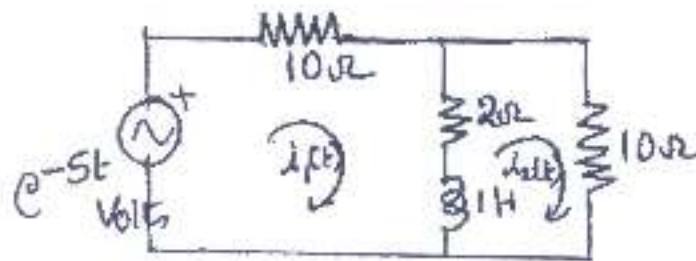
- (a) Find the mesh currents if the coupling factor  $k = 0.6$  10



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(b) Find  $i_2(t)$  using Laplace transform.

10



(3 Hours)

[Total Marks: 80]

**Instructions** – i) Questions 1 is Compulsory

ii) Out of remaining questions attempt any three questions

iii) Assume suitable additional data if required. bridge

iv) Figures in the bracket to the right hand side indicate full marks.

- Q.1 a) Compare analog instrument with digital instrument. (05)  
 b) Explain selection criteria for transducers. (05)  
 c) Which is fastest ADC and why? (05)  
 d) Describe the various types of sweeps used in CRO. (05)
- Q.2 a) Explain working of LVDT and define its application in displacement measurement. (10)  
 b) Draw neat block diagram of Dual Beam oscilloscope. (10)  
 Give the comparison between Dual Trace and Dual Beam Oscilloscope.
- Q.3 a) Draw and explain Hay bridge and its application for measurement of inductances. (10)  
 b) Explain principle of operation and working of dual slope DVM. (10)
- Q.4 a) Define power and energy and explain working of a single phase energy meter. (10)  
 b) Draw and explain capacitive transducer for level measurement. (10)
- Q.5 a) Draw the block diagram of generalised measurement system and explain its component. (10)  
 b) Draw and explain Wheatstone bridge and derive expression for measurement of resistance. (10)
- Q.6 a) Explain dual slope ADC with neat block diagram and comment on its speed. (10)  
 b) Define Q factor and explain working of a Q meter for Q factor measurement. (10)

